

Applicant: Takayuki Mizuno et al.
 Serial No.: 10/536,649
 Filing Date: May 27, 2005
 For: INTERFERENCE OPTICAL SWITCH AND VARIABLE OPTICAL ATTENUATOR

Confirmation No.: 2202
 Att'y Docket No.: 14321.75
 Art Unit: 2883

SUPPLEMENTAL INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

Examiner <u>Initial*</u>	Document <u>Number</u>	Issue <u>Date</u>	<u>Name</u>
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Foreign Patent Documents

Examiner <u>Initial*</u>	Document <u>Number</u>	Publication <u>Date</u>	Country or <u>Patent Office</u>	<u>Translation</u>
<u>MH</u> 1	01-158413	06/21/1989	Japan	No
<u>MH</u> 2	64-077002	03/23/1989	Japan	No

Other Documents

(including author, title, pertinent pages, etc.)

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References Cited by Applicants

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

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Examiner: Michael P. Mooney

Date Considered: 02-15-08

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Form PTO-1449

Sheet 2 of 2

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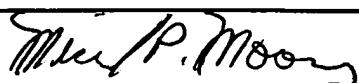
For: INTERFERENCE OPTICAL SWITCH AND VARIABLE OPTICAL ATTENUATOR

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

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<u>MPM</u> 1	WO 02/075978 A1	09/26/2002	PCT	N/A

Other Documents
(including author, title, pertinent pages, etc.)

Examiner
Initial*

MPM 2 Tsutomu Kitoh et al., *Novel Broad-Band Optical Switch Using Silica-Based Planar Lightwave Circuit*, IEEE Photonics Technology letters, Vol. 4, No. 7, July 1992, pp. 735-737.

MPM 3 Takashi Goh et al., *Low Loss and High Extinction Ratio Strictly Nonblocking 16 x 16 Thermooptic Matrix Switch on 6-in Wafer Using Silica-Based Planar Lightwave Circuit Technology*, Journal of Lightwave Technology, Vol. 19, No. 3, March 2001, pp. 371-379.

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